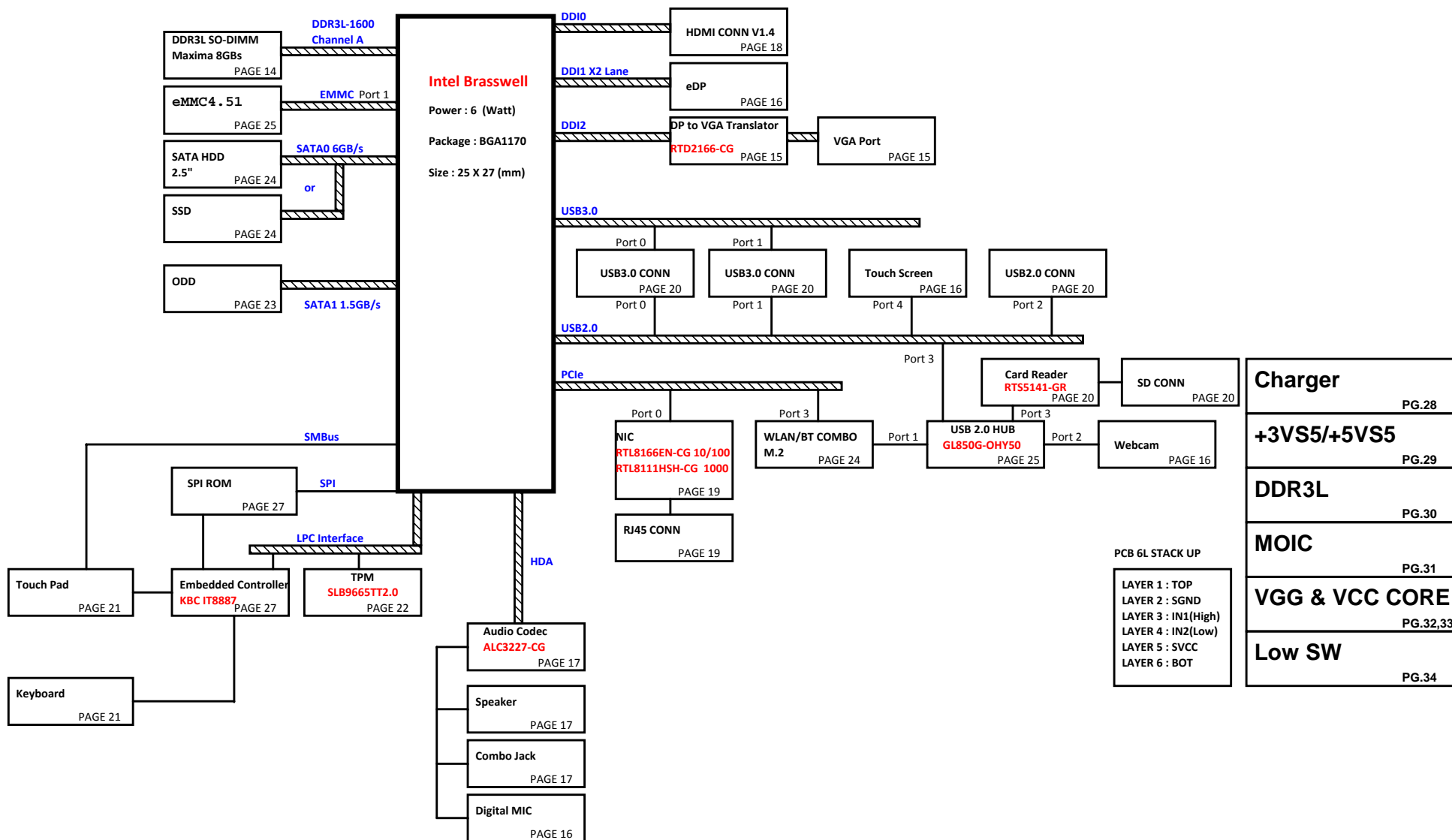
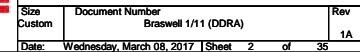
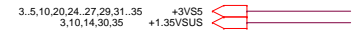
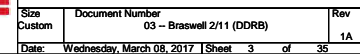


NFL-C BSW 14"

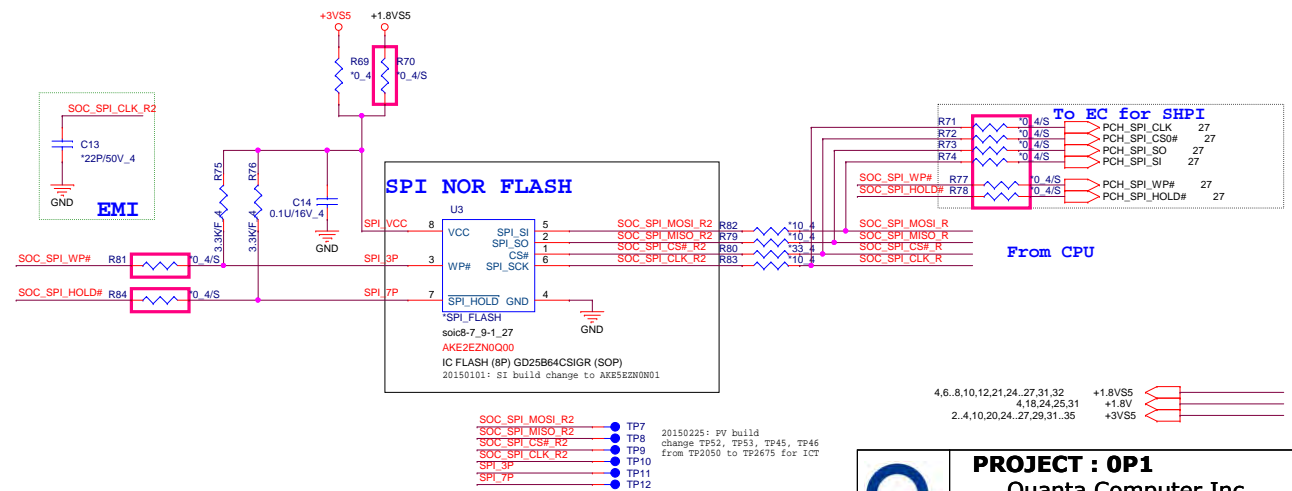
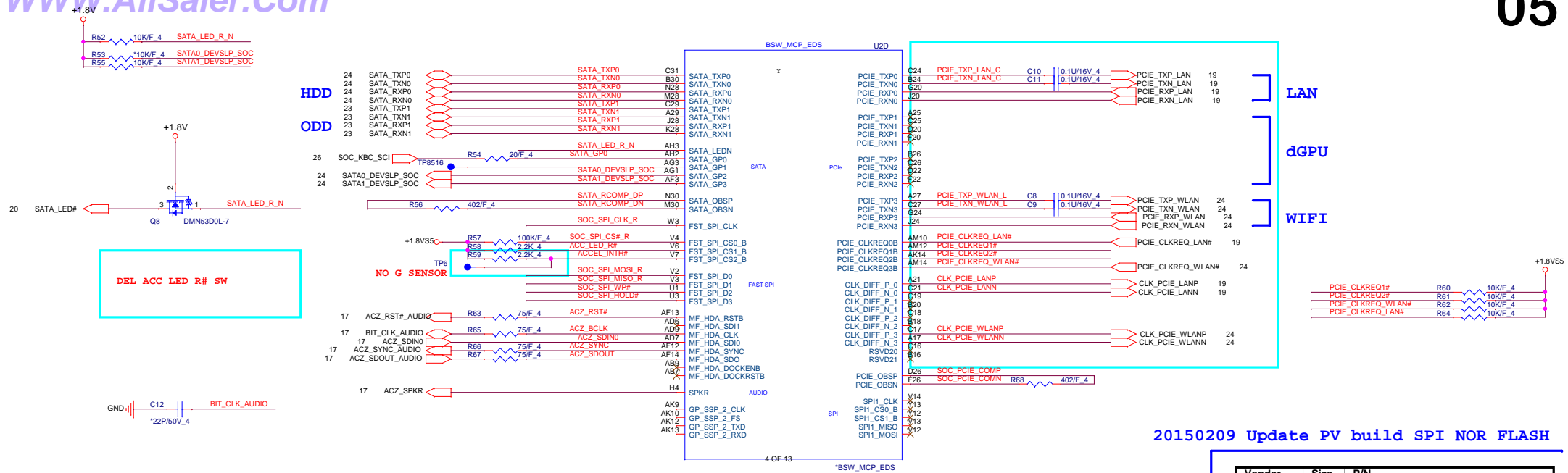
Intel Braswell-M Platform Block Diagram

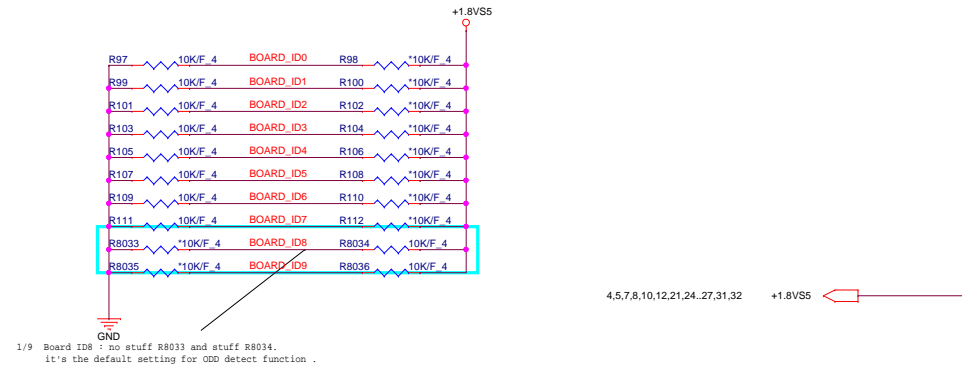









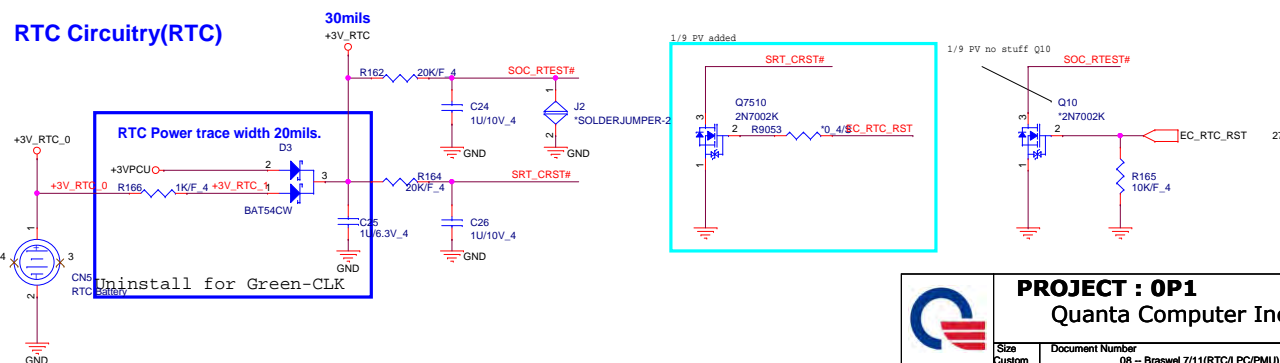
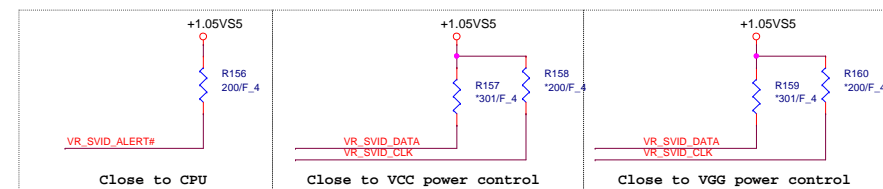




BOARD_ID7	BOARD_ID6	BOARD_ID5	Vender	Quanta PN	Description
0	0	0	Hynix-2GB	AKD5PGSTW14	IC SDRAM (96P) H5TC4G63CFR-PBA TOPBSQ
0	0	1	Micron-2GB		
0	1	0	Samsung-2GB Q Dye		
0	1	1	Samsung-2GB E Dye	AKD5JG0T503	IC SDRAM(96P)K4B4G1646E-BYK0(F8GA)TOPBSQ
1	0	0			
1	0	1	Micron-2GB P Dye	AKD59GSLT04	IC SDRAM(96P) MT41K256M16TW-107:P TOPBSQ
1	1	0			
1	1	1			

	PROJECT : 0P1 Quantia Computer Inc.		
	Size Custom	Document Number 05 -- Braswel 4/11(SP/PC/SA/AU)	Rev 1A
	Date: Wednesday, March 08, 2017 Sheet 6 of 35		

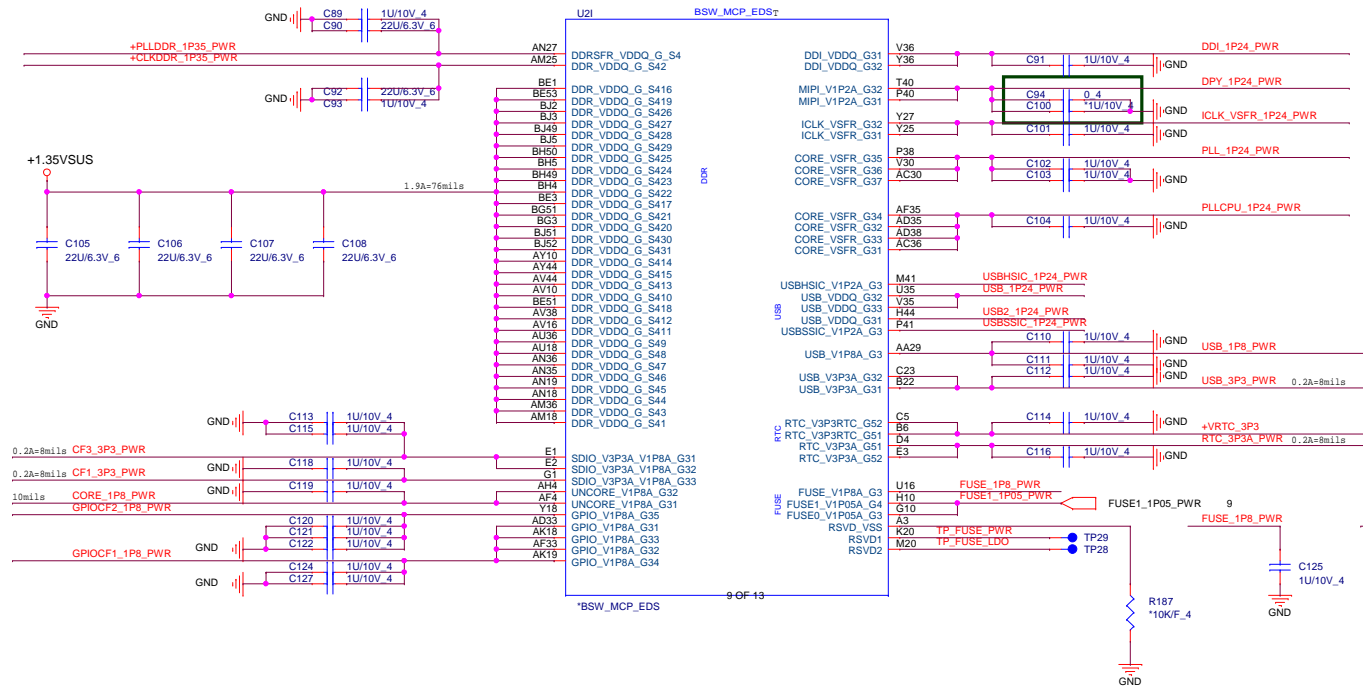




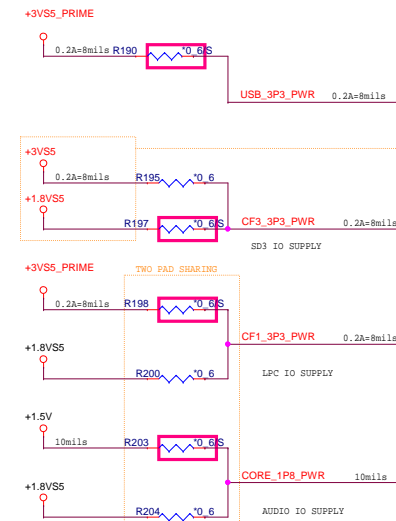
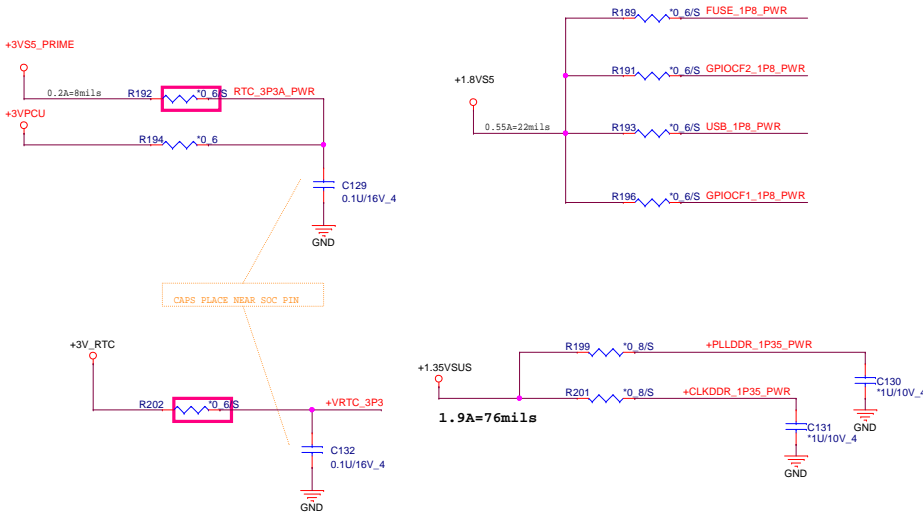
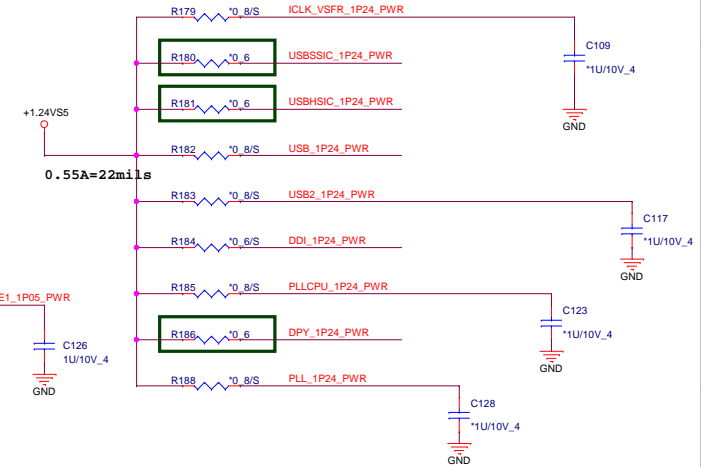
WWW.AliSaler.Com



PLACE THE CAPS UNDER THE PKG SHADOW



PV: Disable HSIC SSIC MIPI power.

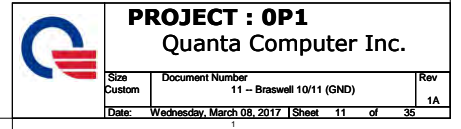


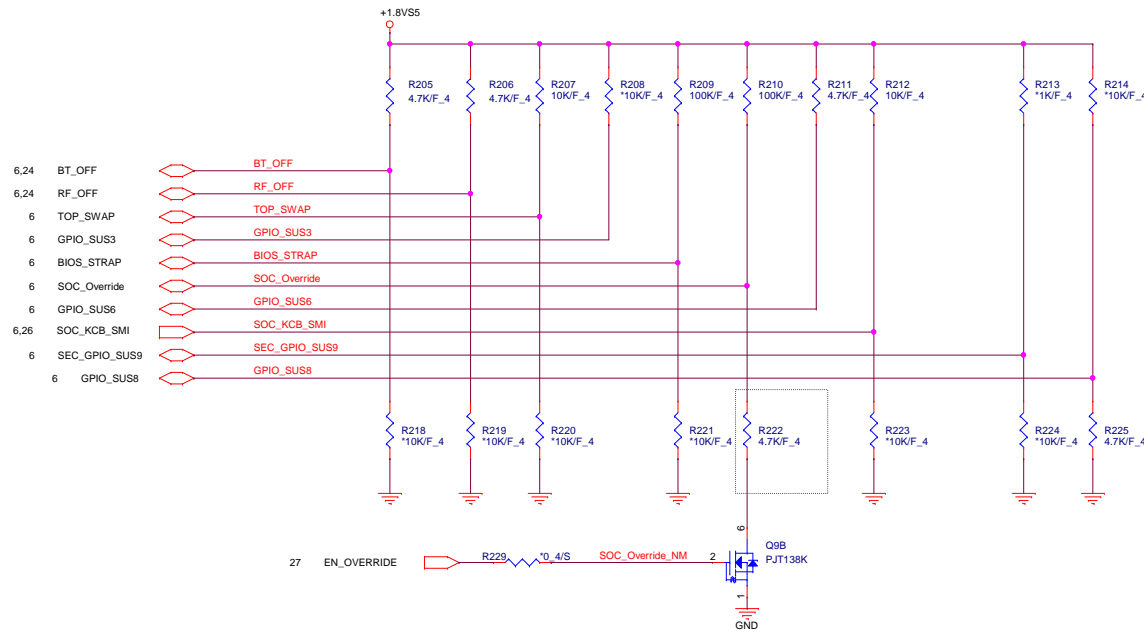
VSDIO VOLTAGE SETTING (CF3_3P3_PWR)		
SDMMC3_PWR_EN_N	SDMMC3_1P8_EN	VSDIO (V)
1	0	0V
1	1	0V
0	0	3.3V
0	1	1.8V

4,8,12,21,24,27,31,32	+1.8VSS
9,31,33	+1.05VSS
9,31	+1.15VSS
31	+1.24VSS
27,31	+3VSS PRIME
2,5,20,24,27,29,31,35	+3VSS
2,3,14,30,35	+1.35VSSUS
8	+3V_RTC
8,20,21,24,26,29	+3VPCU

PROJECT : 0P1
Quanta Computer Inc.

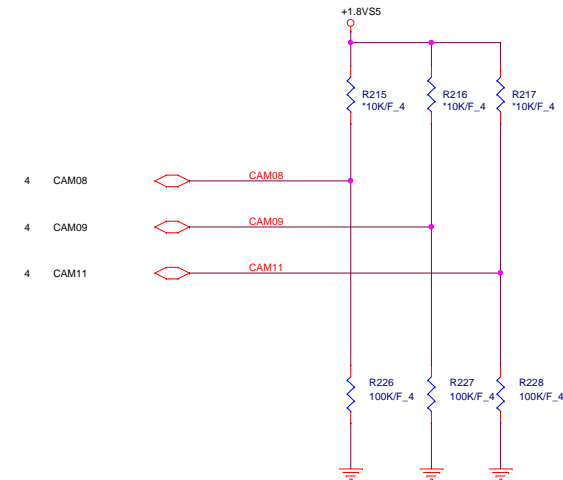
Size Custom	Document Number 10 - Braswell 9/11 (Power 2)	Rev 1A
Date: Wednesday, March 08, 2017	Sheet 10	of 35





REQUIRED STRAPS

	GPIO_SUS0	GPIO_SUS1	TOP_SWAP	GPIO_SUS3	BIOS_STRAP	SOC_Override	GPIO_SUS6	SOC_KCB_SMI	GPIO_SUS8
PULL HIGH	DDI0 detected DEFAULT	DDI1 detected DEFAULT	Normal Operation DEFAULT	Reserve 10 KQ PU DEFAULT	SPI DEFAULT	Normal Operation DEFAULT 20150209 PV change	10 KQ PU to 1.8V DEFAULT	Reserve 10 KQ PU DEFAULT	Supply is 1.35V
PULL LOW	DDI0 not detected	DDI1 not detected	Change Boot Loader address		LPC	Override			Supply is 1.25V DEFAULT




	CAM08	CAM09	CAM11
PULL HIGH	ICLK Xtal OSC Bypass	CCU SUS RO Bypass	RTC OSC Bypass
PULL LOW	ICLK Xtal OSC No Bypass DEFAULT	CCU SUS RO No Bypass DEFAULT	RTC OSC No Bypass DEFAULT

4,8,10,21,24,27,31,32 +1.8VS5

PROJECT : 0P1
Quanta Computer Inc.

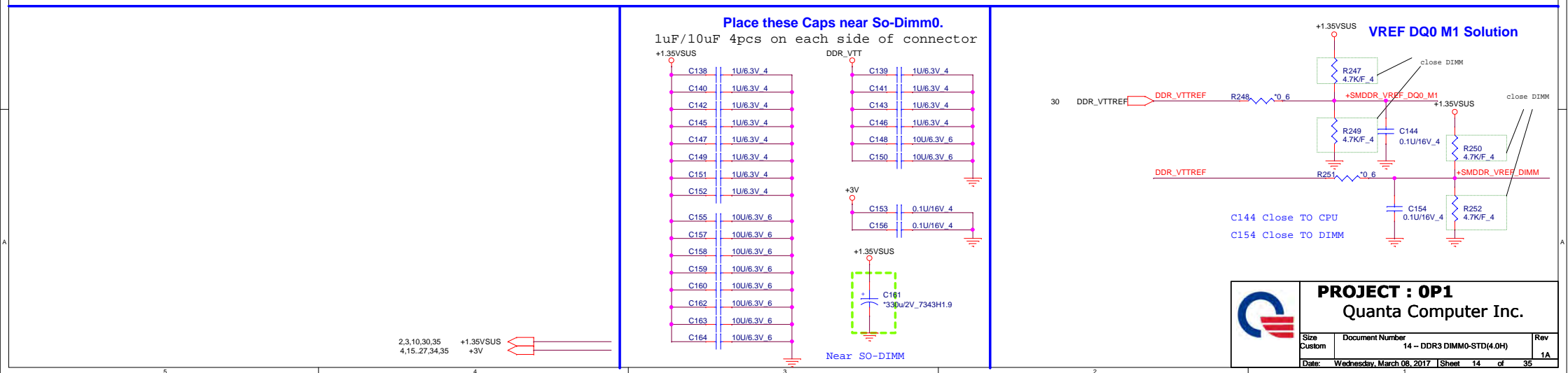
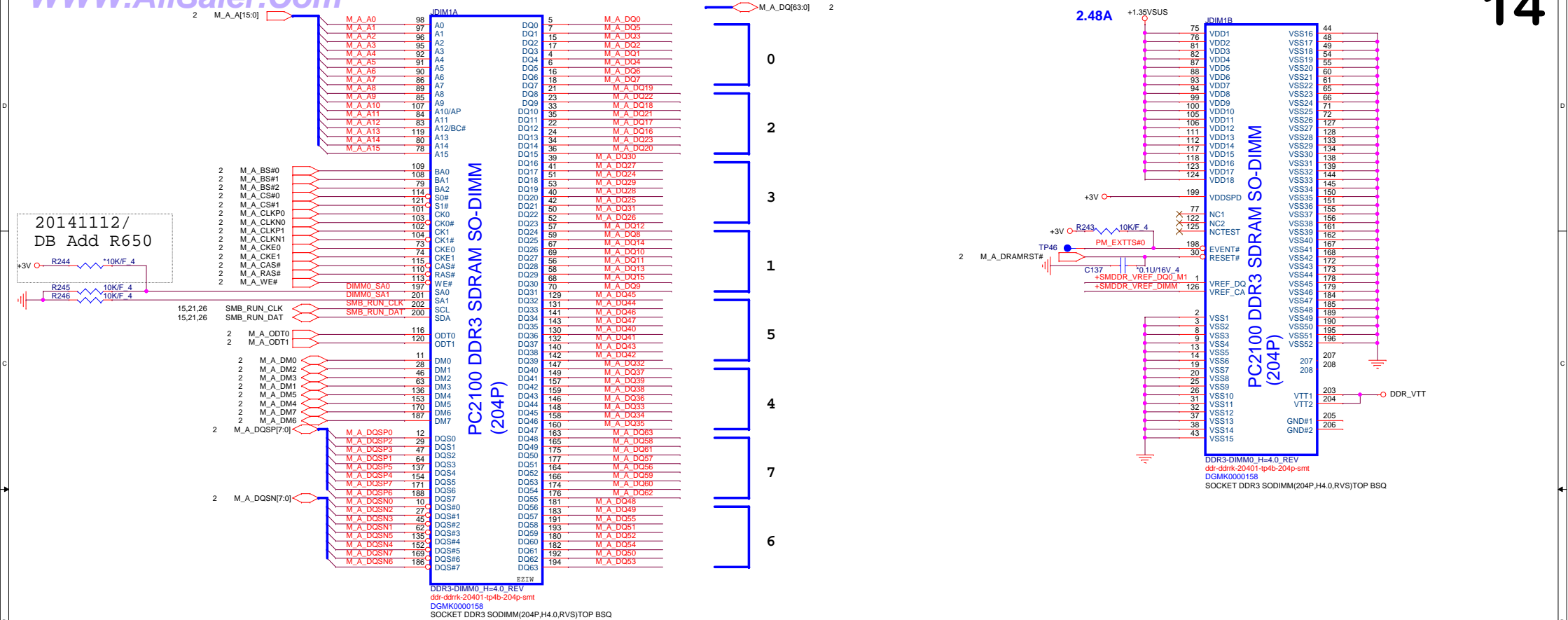
Size Custom	Document Number 12 - Braswell 11/11 (Strap)	Rev 1A
Date:	Wednesday, March 08, 2017	Sheet 12 of 35

20141031: DB
Del U15, U39, U38, U37, U40
Del R216, R662, R159, R656, R709



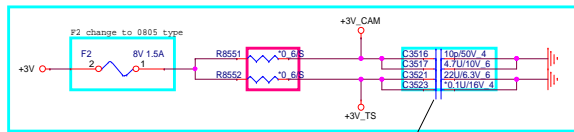
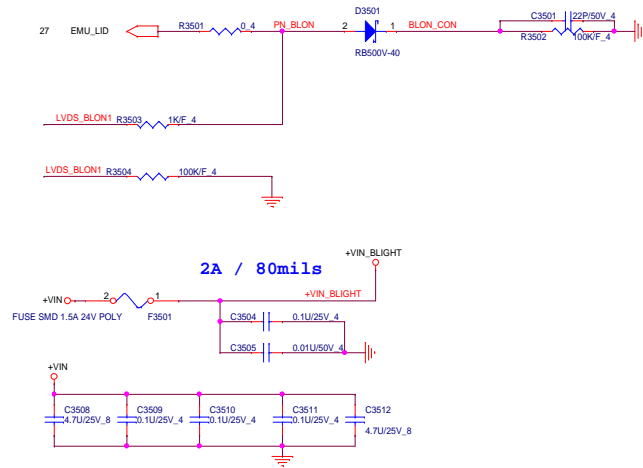
PROJECT : 0P1
Quanta Computer Inc.

Size C	Document Number 13 - BSW XDP/APS	Rev 1A
Date: Wednesday, March 08, 2017 Sheet 13 of 35		

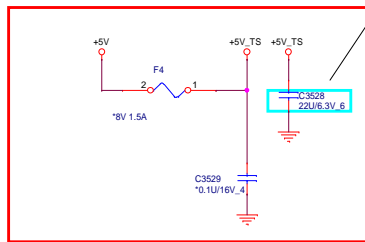




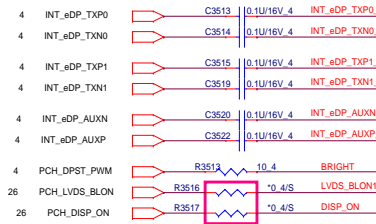
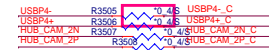
LID Switch



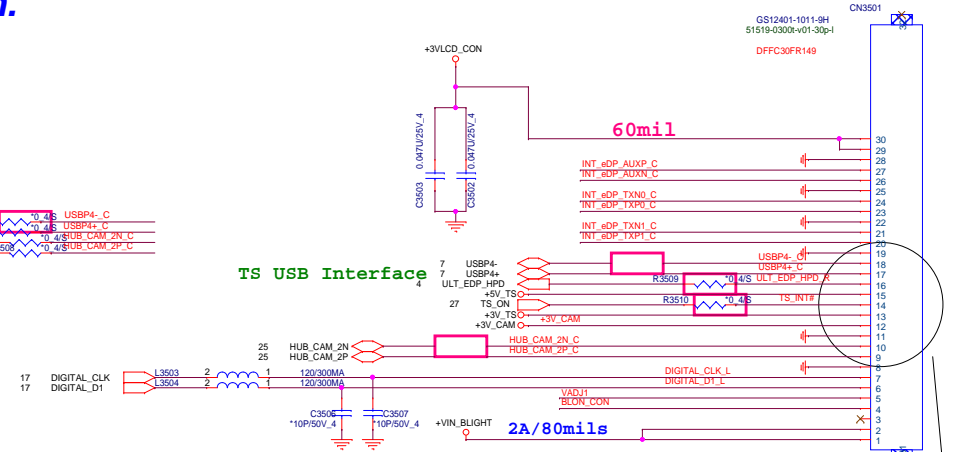
1/9 PV Stuff C3517,C3521,C3528 and C3516 used 10p



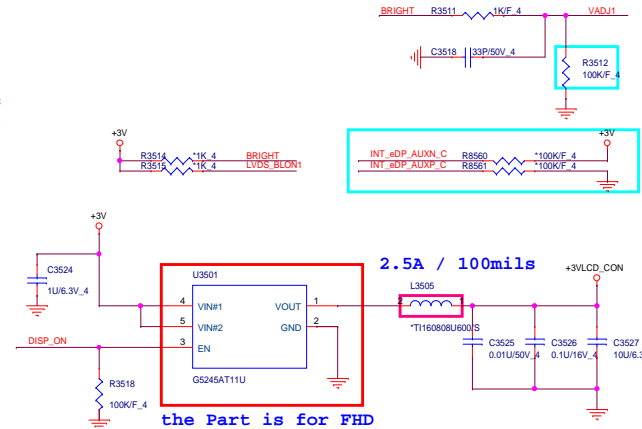
eDP Conn.



TS USB Interface



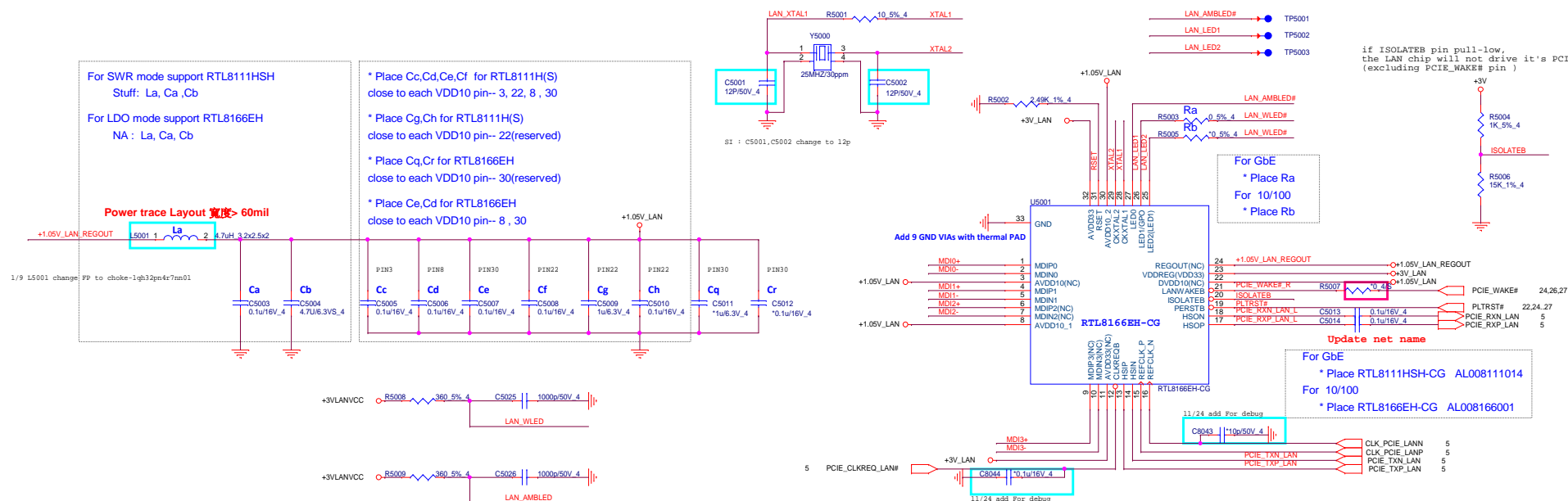
11/18 update LVDS CONN PIN define



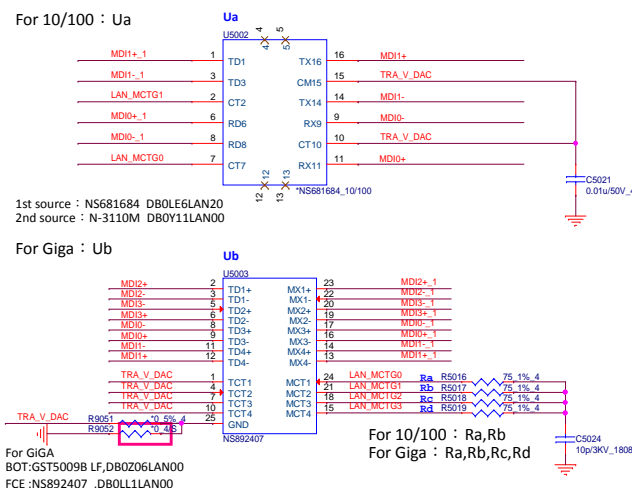
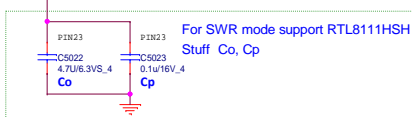
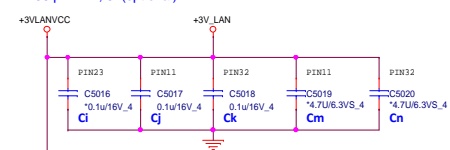
4,14,15,17,27,34,35
15,17,18,21,23,24,34
23,28,33,35

	PROJECT : 0P1		
	Quanta Computer Inc.		
	Size Custom	Document Number 16 - eDP CONN/LID/CAMD-MIC/TS	Rev 1A
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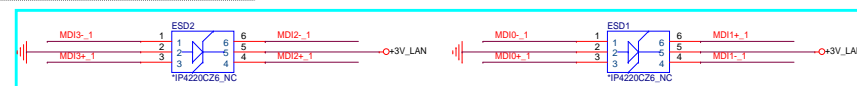
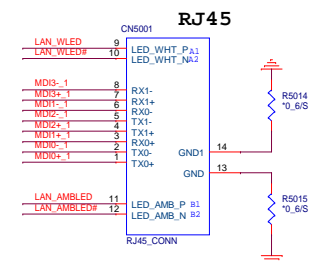
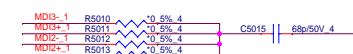




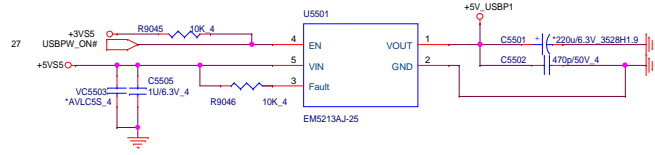
- * Place Ci and Ck, close to each VDD33 pin-- 23, 32 for RTL8166EH
- * Place Cj and Ck, close to each VDD33 pin-- 11, 32 for RTL8111H(S)
- * For surge improvement, place Cm and Cn, close to each VDD33 pin-- 11, 32(optional)



For 10/100 stuff only & Close RJ45

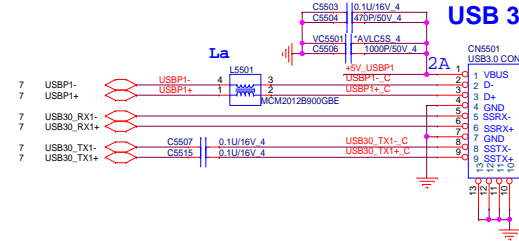


For 10/100 : ESD1
For Giga : ESD1,ESD2

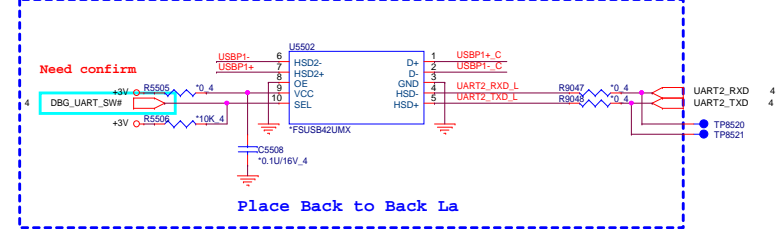


USB 2.0/3.0 Combo

USB 3.0

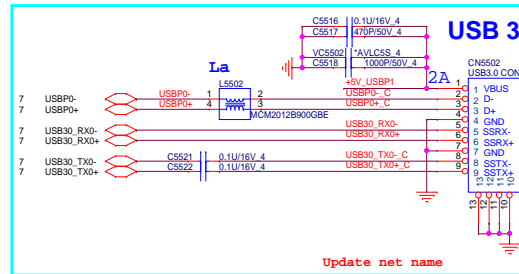


UART for Win7 WHQL DEBUG

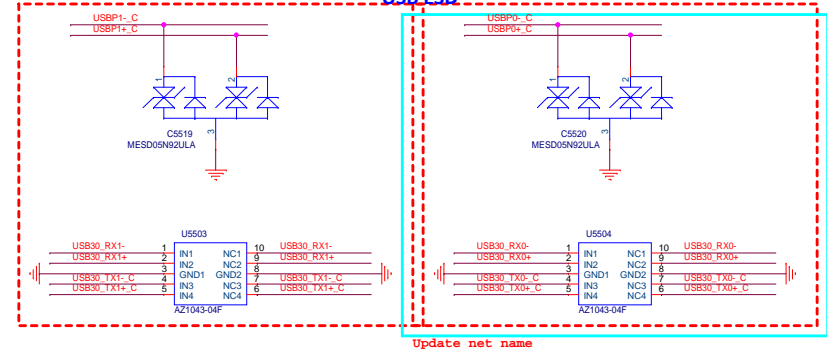


USB 2.0/3.0 Combo

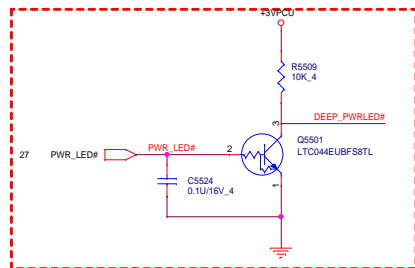
USB 3.0



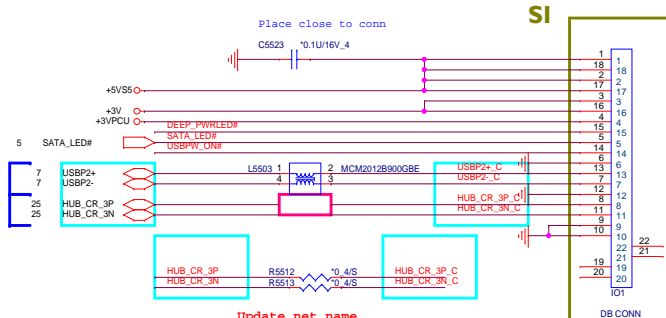
USB ESD



Daughter Board

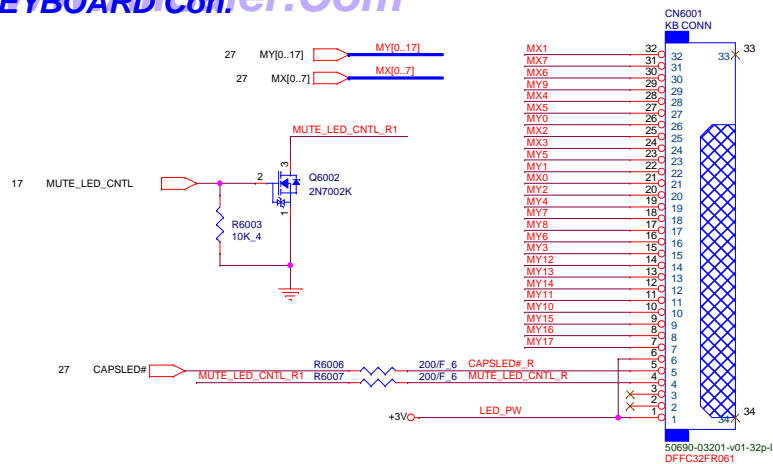


USB2 CR

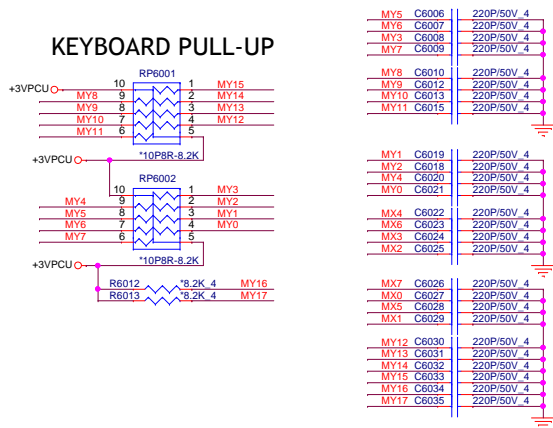


SI

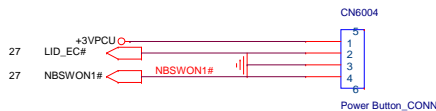
4.14, 19.21, 27.34, 35 +5VSS
8.10, 21.24, 26.29 +3V
+5VPCU



KEYBOARD PULL-UP

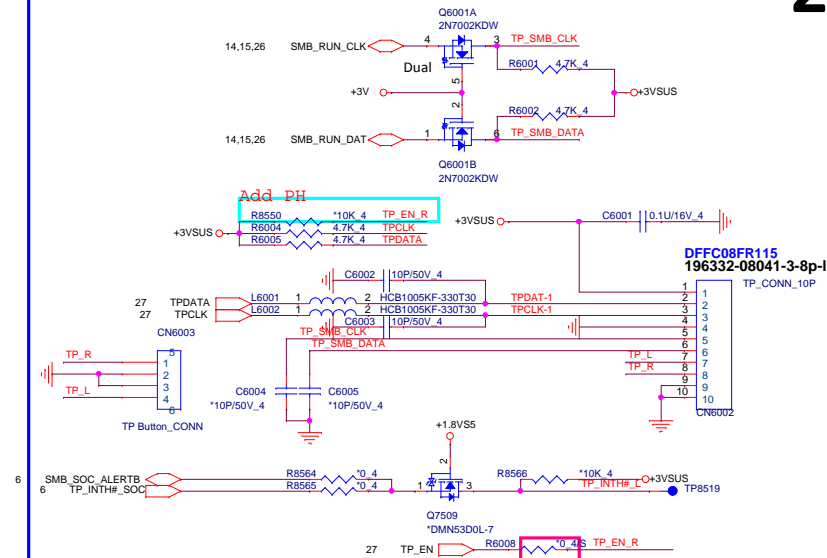


Power Button

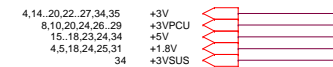
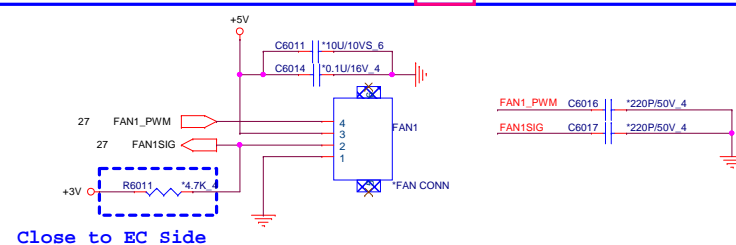


Touch Pad Connector

21



FAN



PROJECT : 0P1		
Quanta Computer Inc.		
Size	Document Number	Rev
Custom	21 -- KB/TP/FAN/POWER	1A
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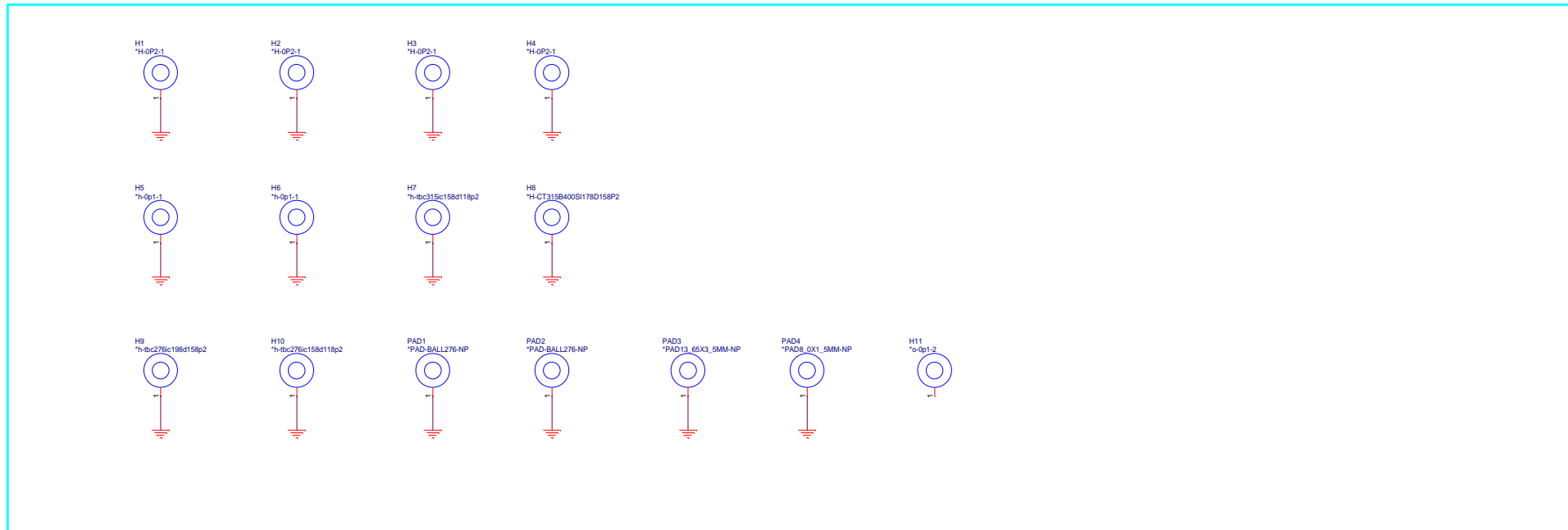
+3V



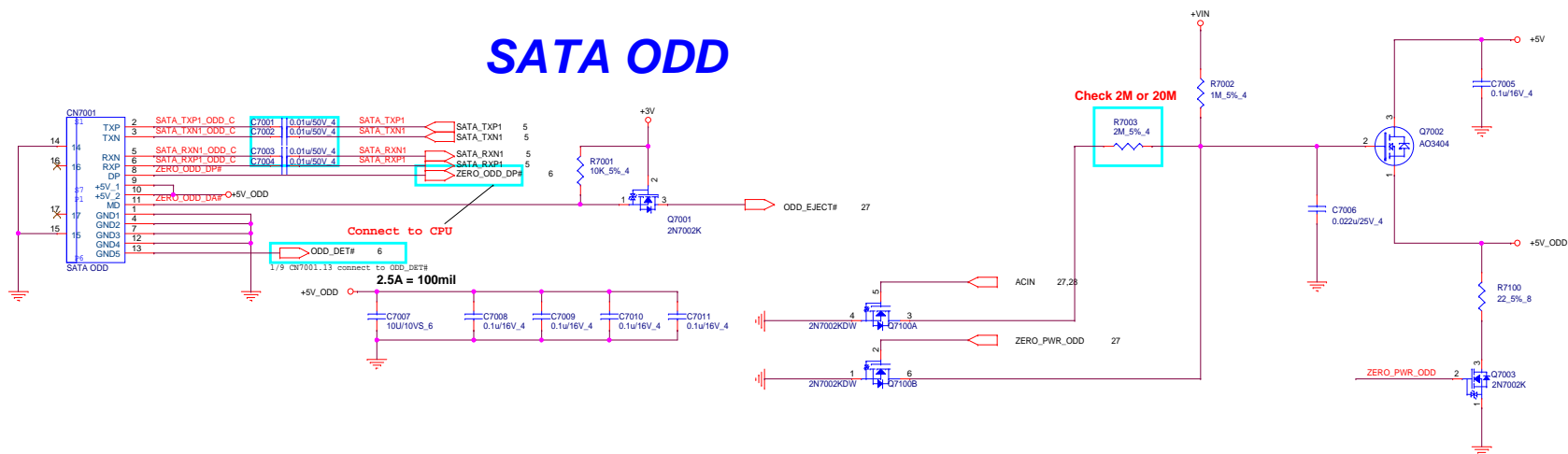
PROJECT : 0P1
Quanta Computer Inc.

Size B	Document Number 22 -- TPM	Revision 1/
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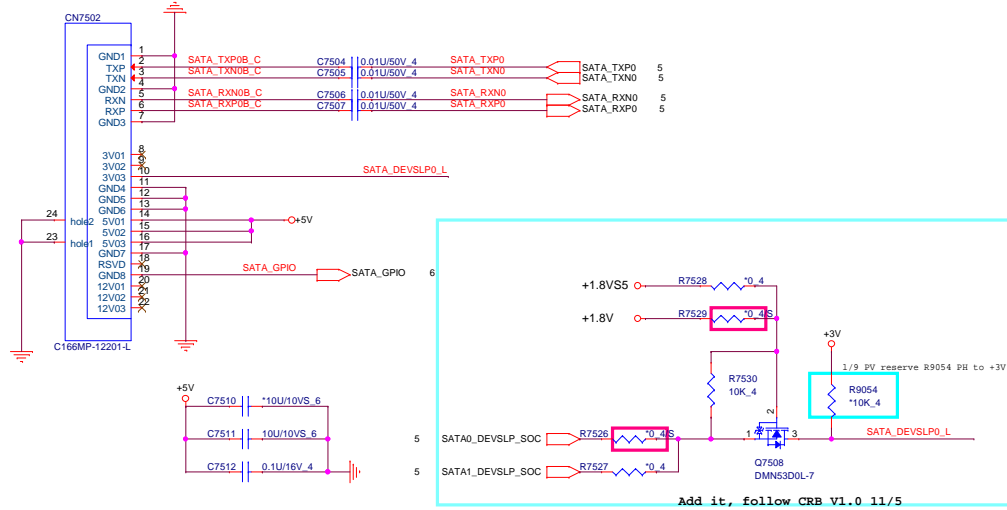
Hole



SATA ODD

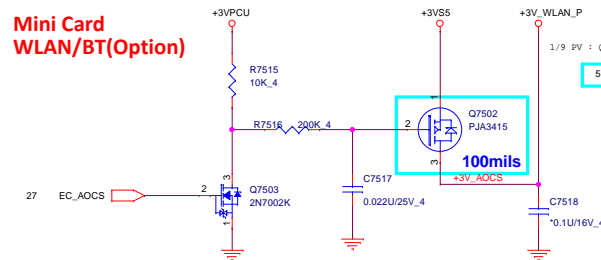


SATA HDD

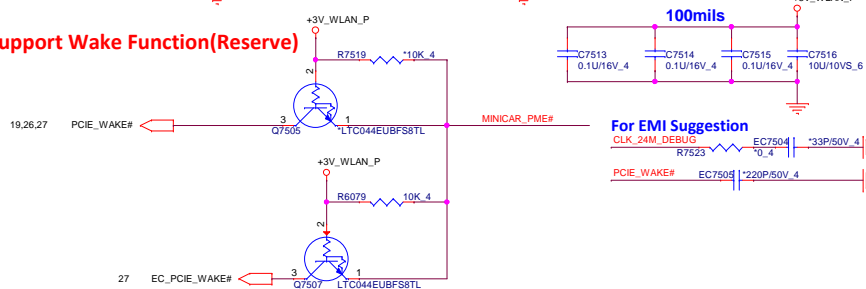


WLAN

Mini Card WLAN/BT(Optional)

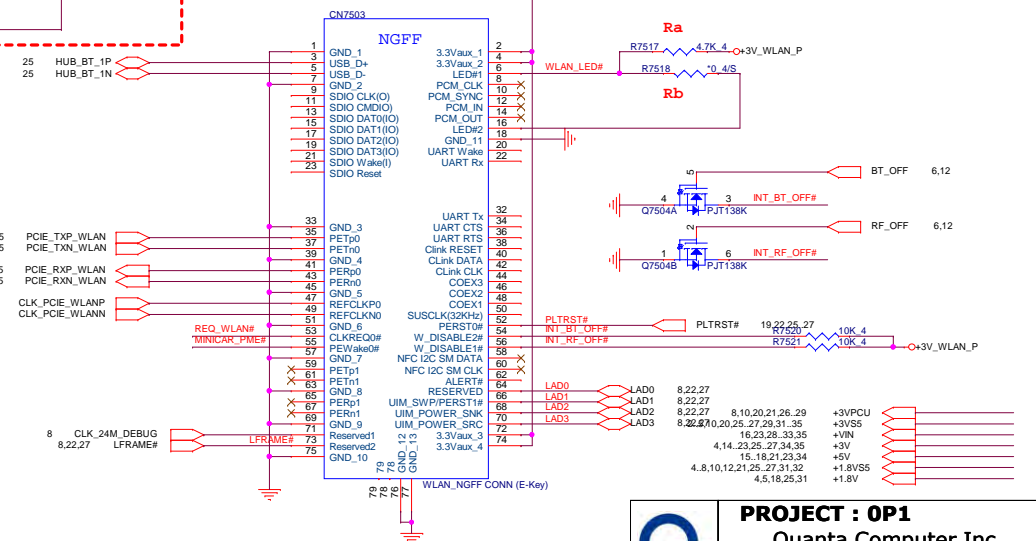


Support Wake Function(Reserve)

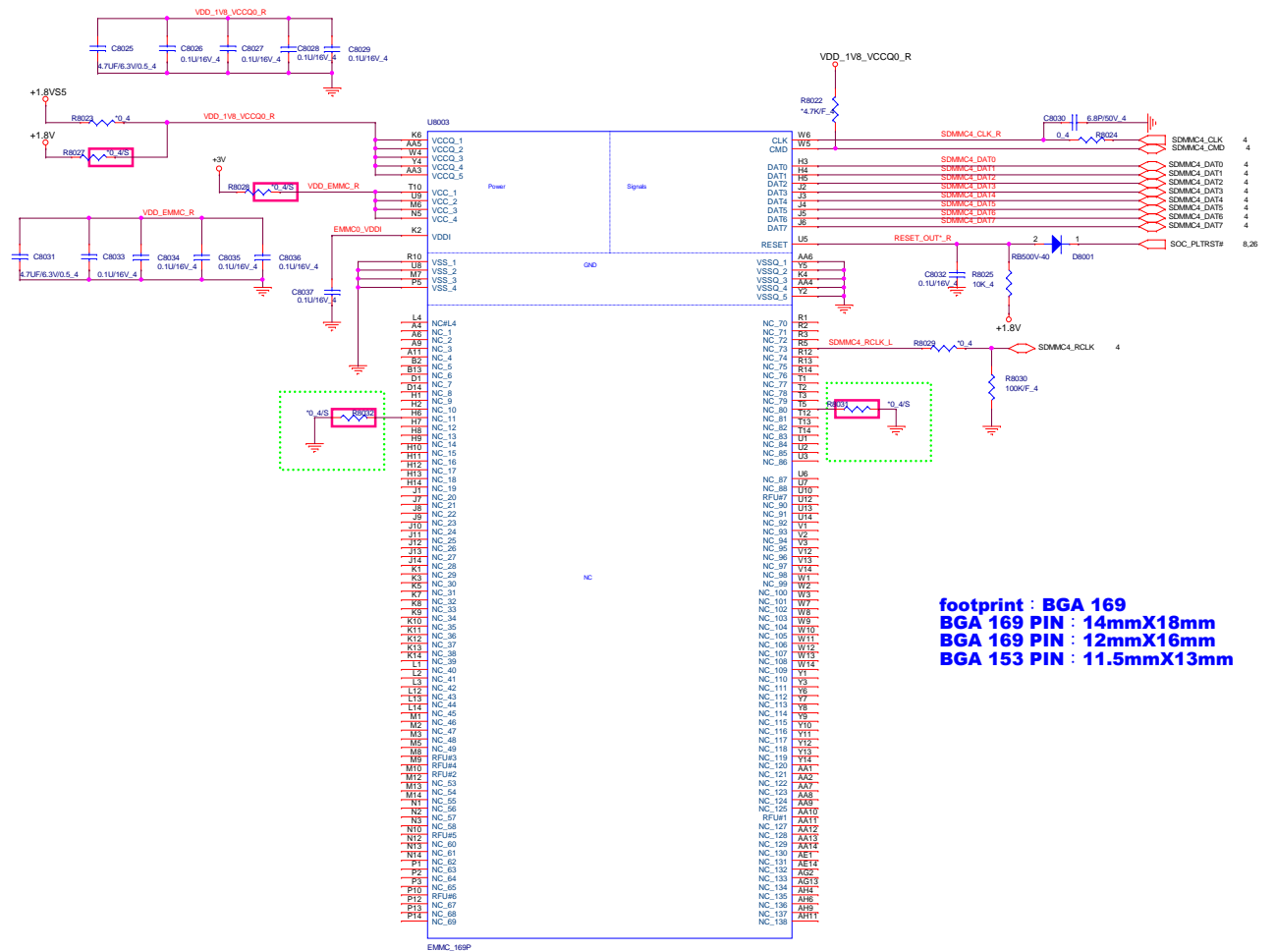
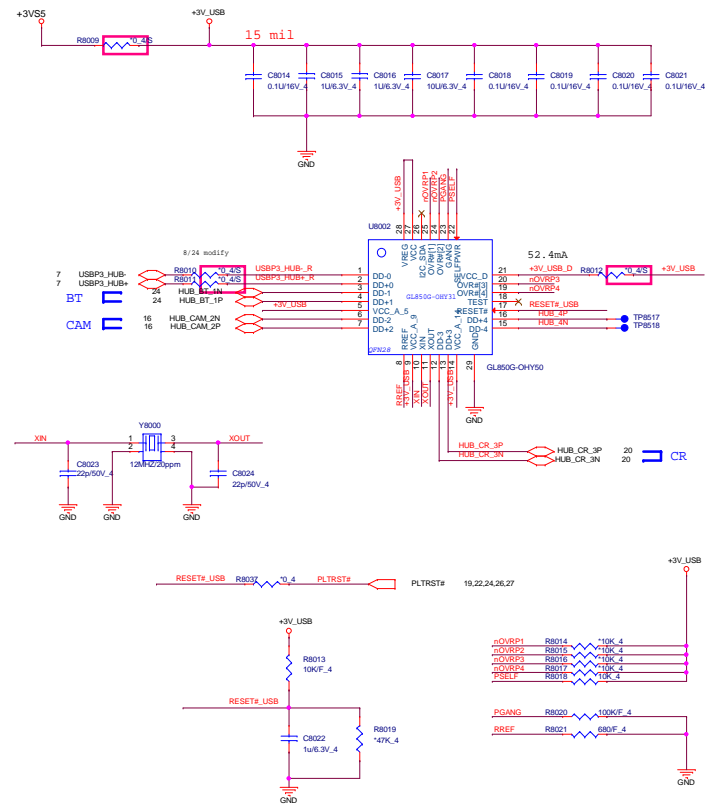


0302 Reserved the MOSFET at CLKREQ# even the current leakage test passed for HP requested

Remove Net RF_LINK# and need check if Ra and Rb can be NI



PROJECT : 0P1			
Quanta Computer Inc.			
Size	Document Number	24 - HDD(WLAN(NGFF))	Rev
Custom			1A
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27741 - eMMC 32GB MO-276 MMCv5.0					
TOP850	QBCON	Description	SIZE	Vender	
AKESJ2T07W01	AKESJ2T07W00	IC FLASH(153P) H26M6420EMR(FBGA) TOP BSQ	32G	Hynix	
AKES3S07501	AKES3S07500	IC FLASH(153P) KLMBG42JENB-B041 TOP BSQ	32G	Samsung	
AKESF3-T107	AKES3F-T106	IC FLASH(153P) SDINAF4-32G-0 TOP BSQ	32G	SanDisk	
AKES520T001	AKES520T000	IC FLASH (153P) THGBMHG8C2BAI1 TOP BSQ	32G	Toshiba	

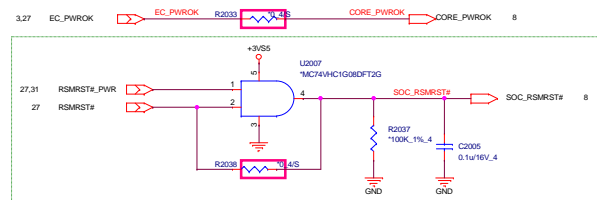
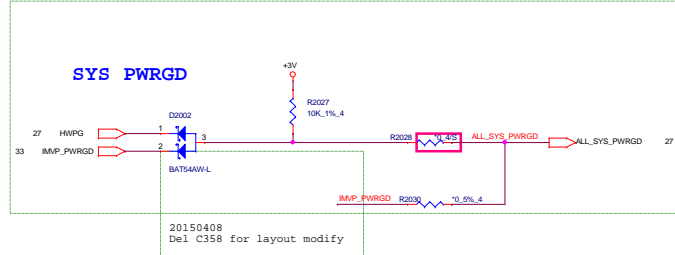
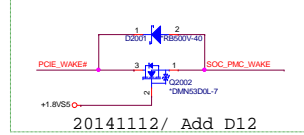
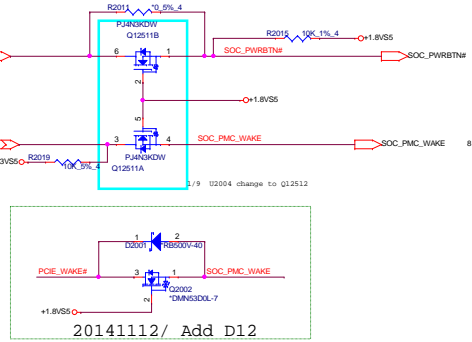
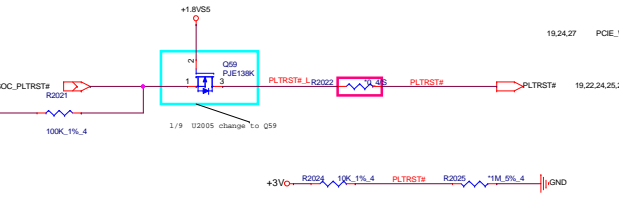
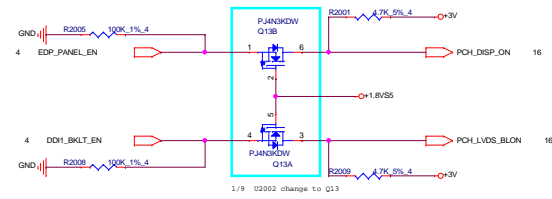
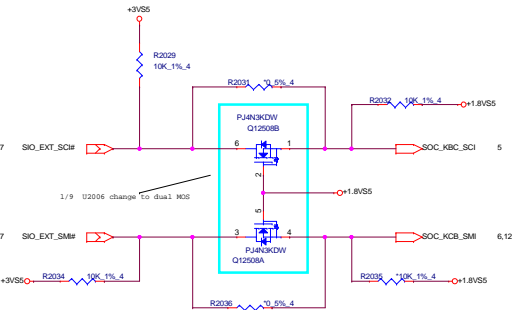
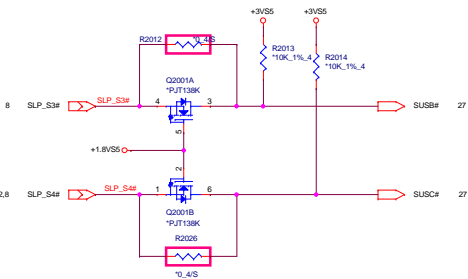
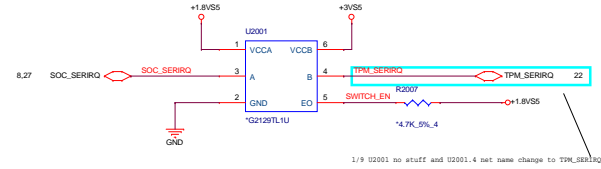
27742 - eMMC 64GB MO-276 MMCv5.0					
TOP850	QBCON	Description	SIZE	Vender	
AKESJ2T07W03	AKESJ2T07W02	IC FLASH(153P)H26M78208CMR(FBGA) TOP BSQ	64G	Hynix	
AKES3U2F501	AKES3U2F500	IC FLASH(153P) KLMCG4JENB-B041 TOP BSQ	64G	Samsung	
AKESJ2TP014	AKESJ2TP013	IC FLASH(153P)THGBMHG9C4BAIR TOP BSQ	64G	Toshiba	

4,14,24,26,27,34,35 +3V
2,5,10,20,24,26,27,29,31...35 +3VS5
4,8,10,12,21,24,26,27,31,32 +1.8VS5
4,5,18,24,31 +1.8V

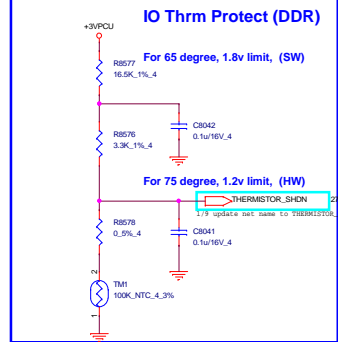
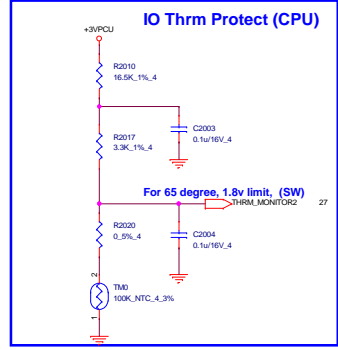


PROJECT : 0P1
Quanta Computer Inc.

Size Custom	Document Number 25 – USB HUB / EMMC	Rev 1A
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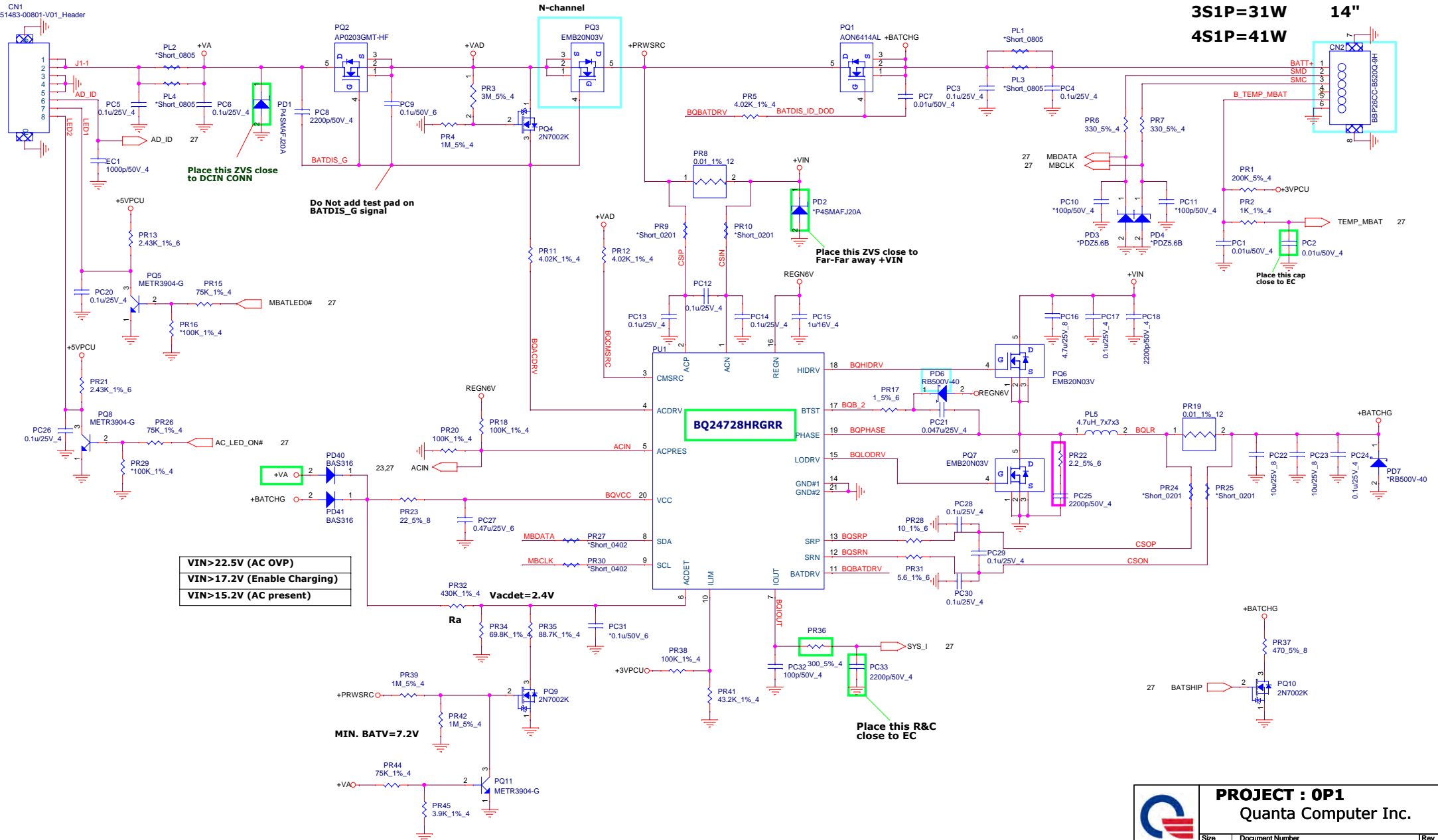
11/5 Add AND gate



8,10,20,21,24,27,29 +3VPCU
4,14,26,27,34,35 +3V
2,5,10,20,24,26,27,29,31,35 +3V5S
4,8,10,12,21,24,25,27,31,32 +1.8V5S
4,5,18,24,25,31 +1.8V

PROJECT : OP1			
Quanta Computer Inc.			
Docu- ment	Number	26 - Level shif/Thermistor	Rev
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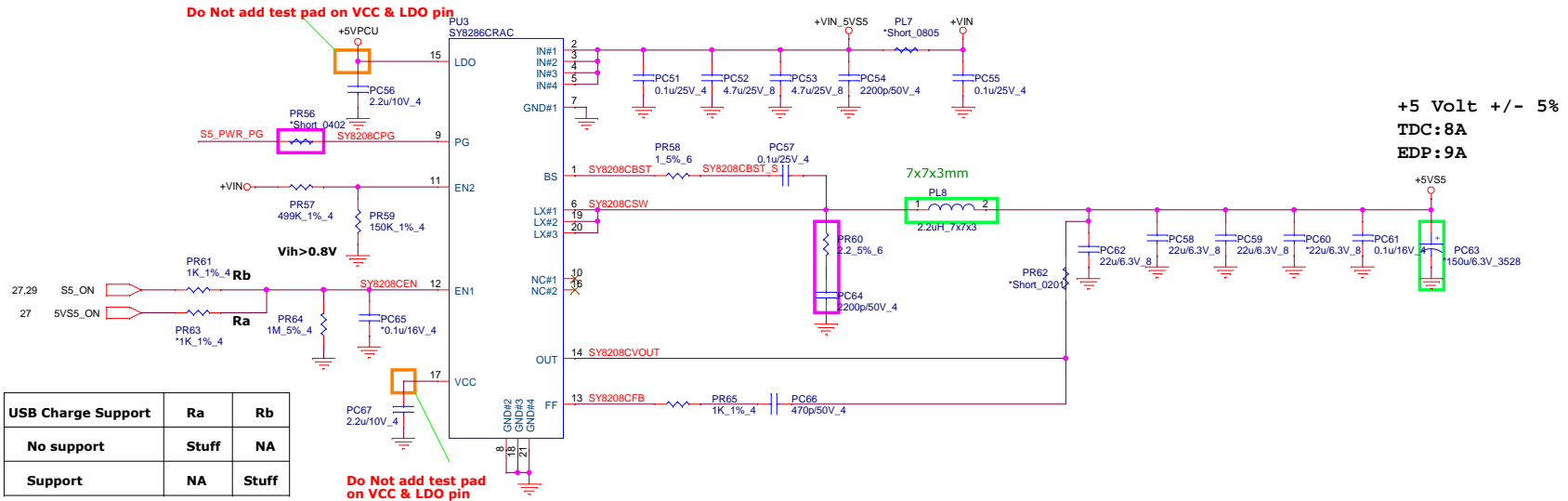
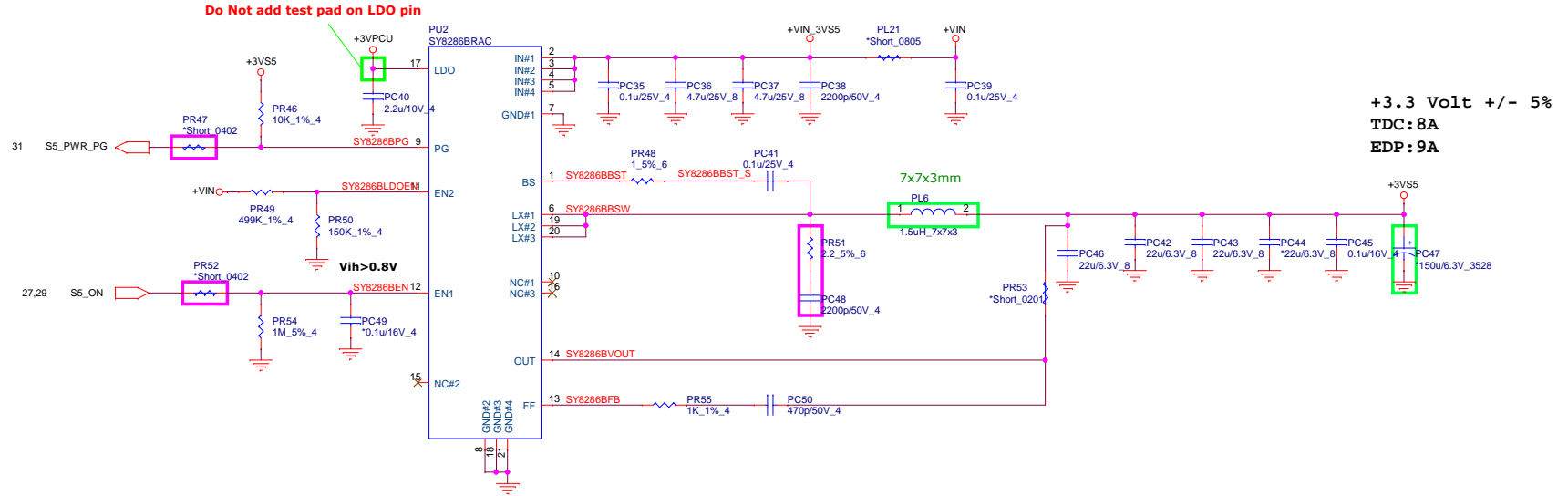
CN1
51483-00801-V01_Header

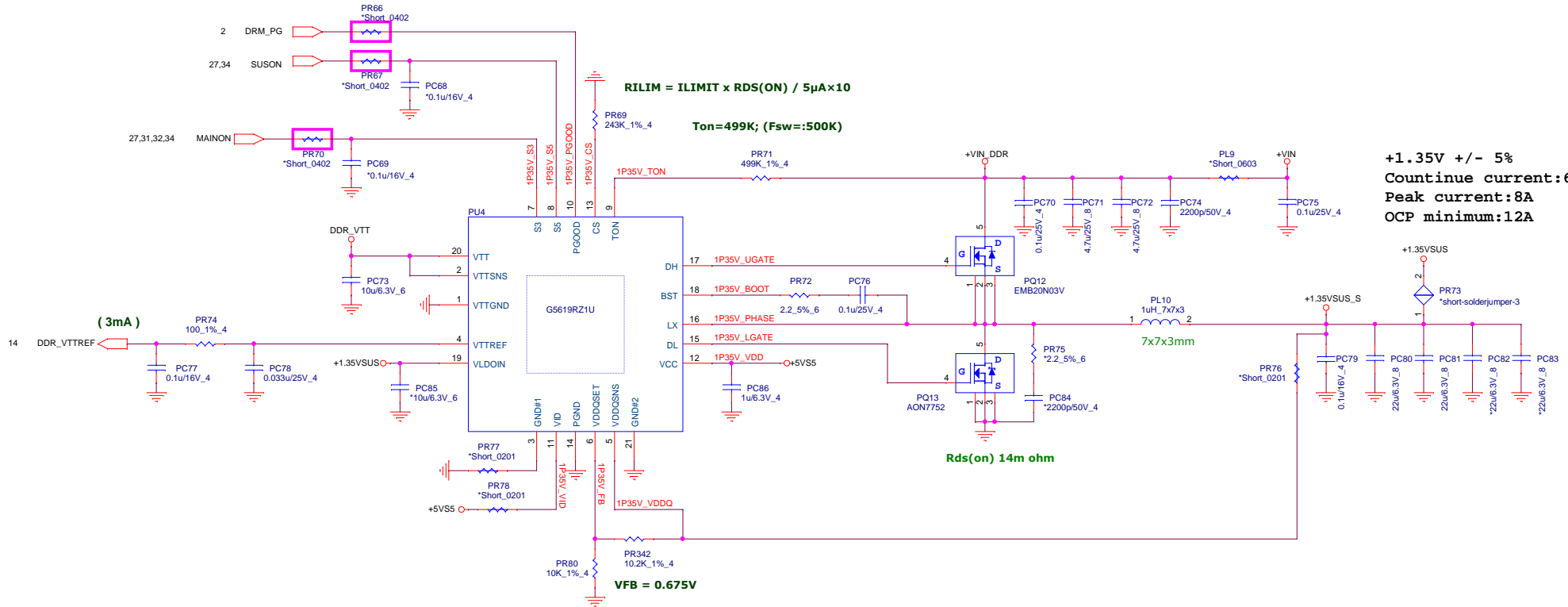


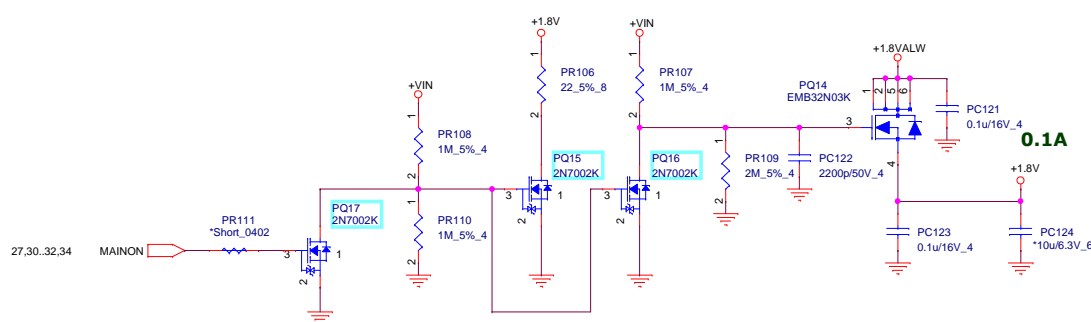
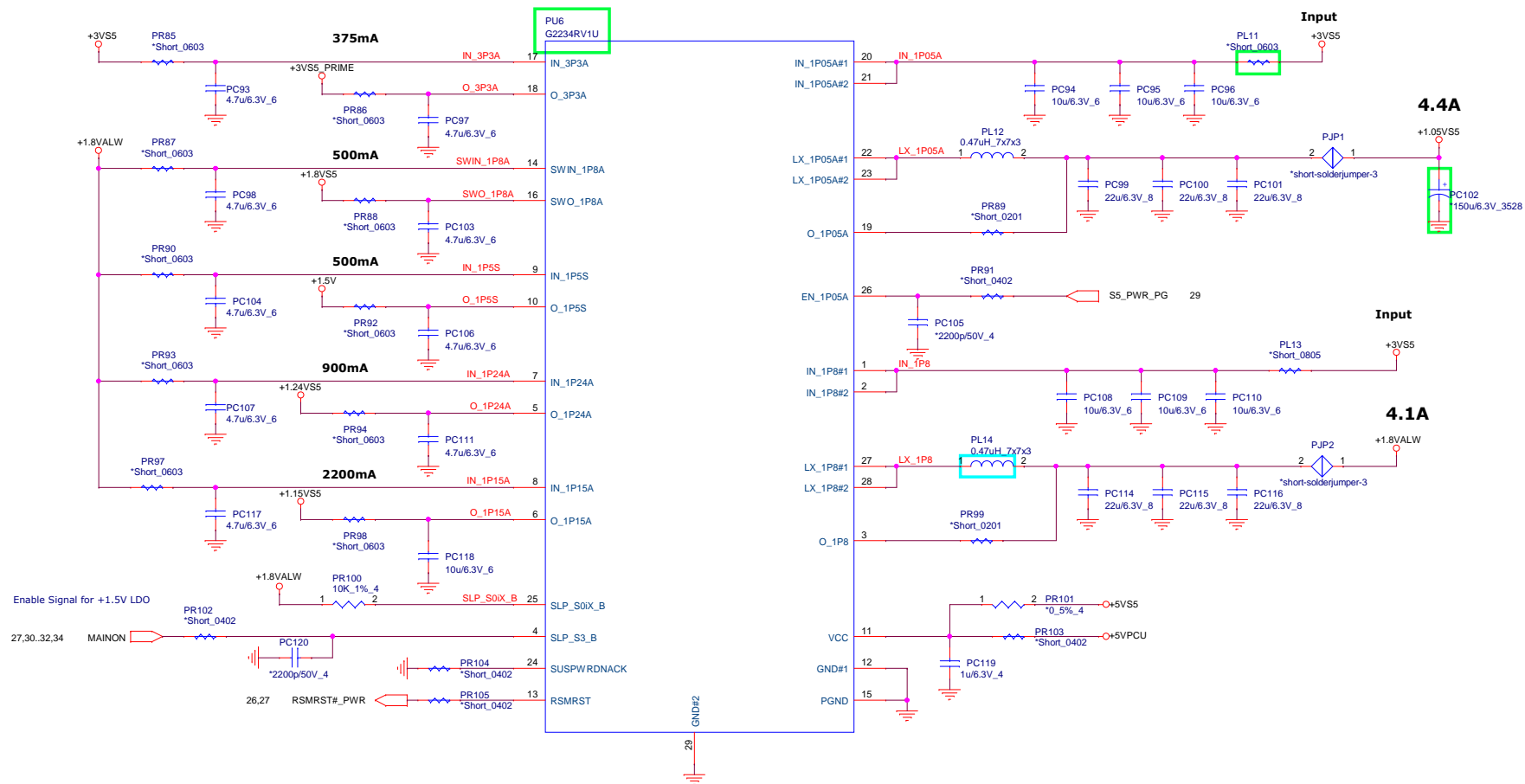
PROJECT : 0P1
Quanta Computer Inc.

Size Custom	Document Number Charger (BQ24738H)	Rev 1A
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+VIN	16,23,28,30,33,35
+3VS5	2,5,10,20,24,27,31,35
+5VS5	20,30,35
+3VPCU	8,10,20,21,24,26,28
+5VPCU	28,31,34

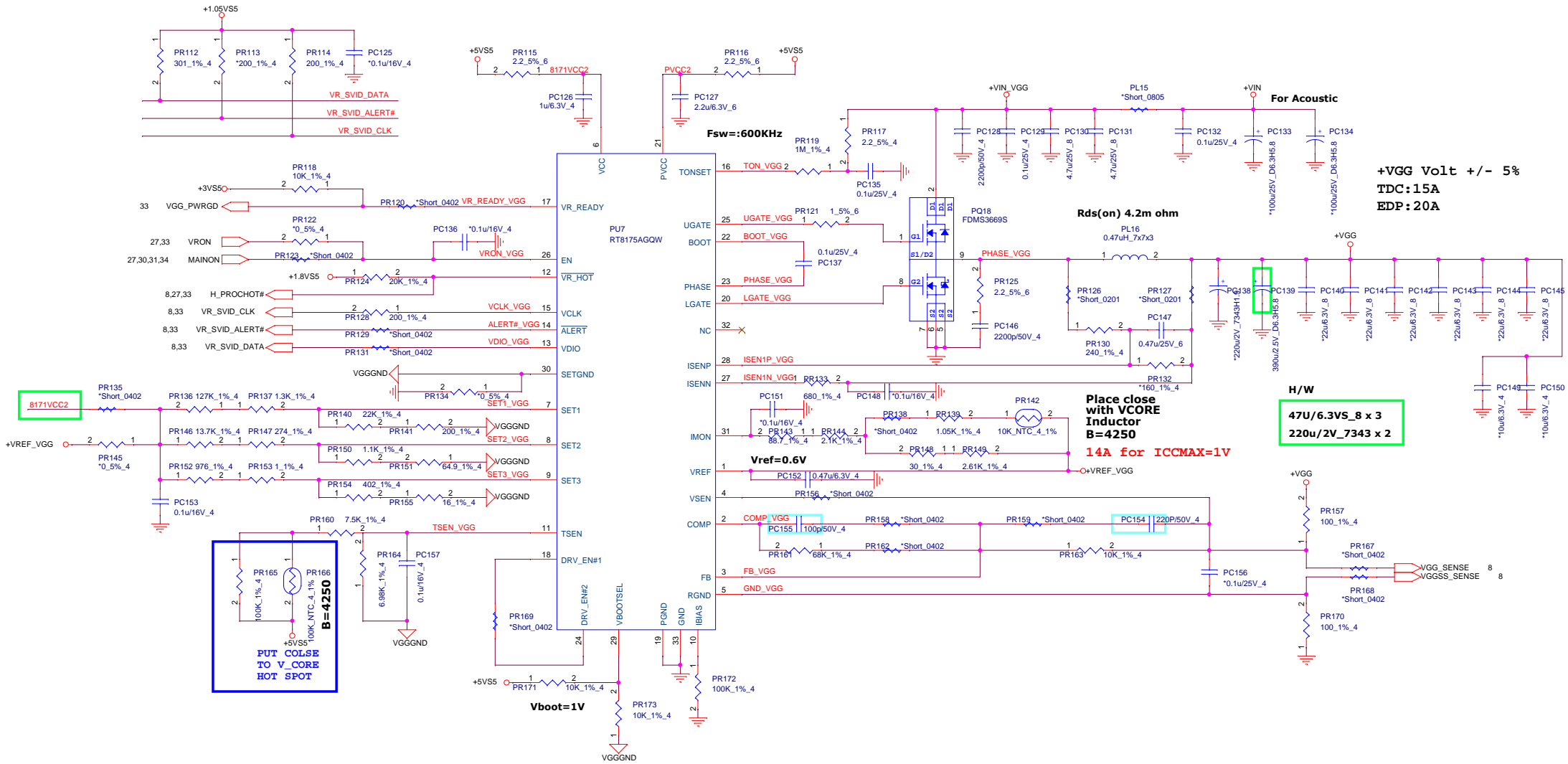


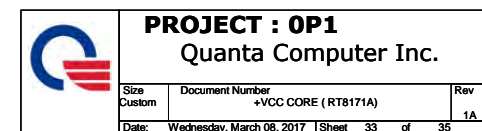




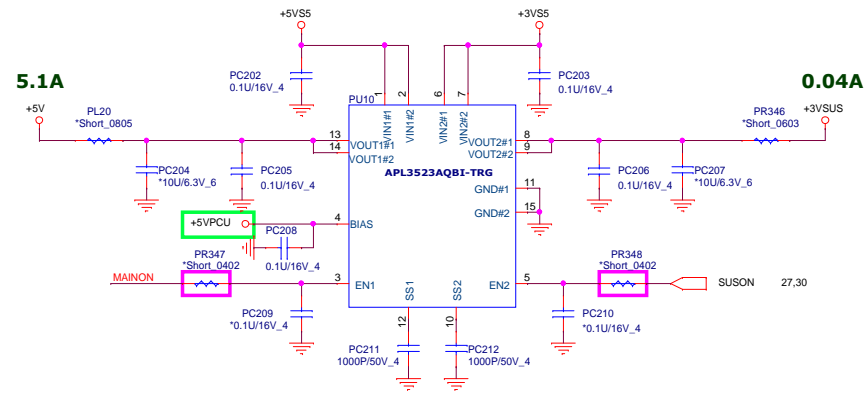
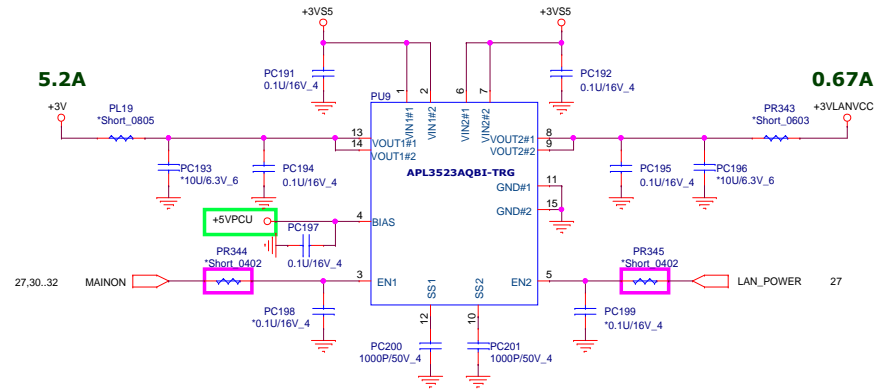
+3VS5	2,5,10,20,24,27,29,32,35
+1.8VALW	4,8,10,12,21,24,27,32
+3VS5_PRIME	10,27
+1.5V	10,17
+1.24VS5	10
+1.15VS5	9,31
+5VPCU	28,29,31,34
+1.05VS5	8,8,32,33
+1.15VS5	9,31
+5VPCU	28,29,31,34
+1.8V	4,5,18,24,25

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Size	Document Number	Rev
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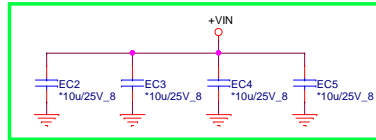




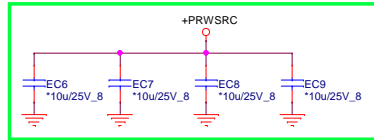
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+5V	15,18,21,23,24
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+5VS5	20,29,33,35
+3VSUS	21
+5VPCU	28,29,31
+3VLAVCC	19



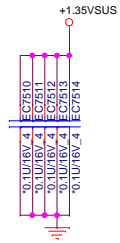
EMI request for ISN



EMI request for ISN




TOP side



BOT side



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Size Custom	Document Number EMI solution		Rev 1A	
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